

Nikolaos Drosinos

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DATE OF BIRTH: 20/07/1978

PLACE OF BIRTH: Athens, Greece

EDUCATION

- 2001-2006: National Technical University of Athens - School of Electrical and Computer Engineering: PhD thesis "Methods and Techniques for the Automatic Generation of Parallel Code for Nested Loop Algorithms".
- 1996-2001: National Technical University of Athens - School of Electrical and Computer Engineering: Diploma of Electrical and Computer Engineer (diploma degree: 8,52/10,00, panhellenic admission rank: 8th).
- 1990-1996: German School of Athens (graduation degree: 18/20).
- 1984-1990: Erasmios Greek-german School.

PROFESSIONAL EXPERIENCE

- 2007-today: Intracom S.A. Telecom Solutions - Department of Wireless Network Systems: embedded software development for wireless/wireline telecom systems in Linux/C++.
- 2003-2004: National Technical University - Computing Systems Laboratory: "Greek Distributed Digital Library for Grey Literature" (EPEAEK). UNIX system administration.
- 2003: National Technical University - School of Electrical and Computer Engineering: design and implementation of School's Portal for Diploma Thesis and Technical Reports. Software development in Perl.
- 2002: National Technical University of Athens - Institute of Communication and Computer Systems: research project SMART P.I.V. "Development of an Interactive Integrated P.I.V. System based on Miniaturised Optical Sensor Technology for Implantable Biomedical Devices Design" (IST-2001-37548).
- 2001-2002: National Center for Scientific Research "Demokritos" - Institute of Informatics and Telecommunications: research project WIN "Wireless Internet Network" (IST-1999-20947) for Wireless Broadband Networks. Software development in Visual Basic.
- 1999: RAM Magazine (computer magazine): development of hard disk performance benchmarks in Visual C++.

TEACHING EXPERIENCE

- 2001-2005: Teaching assistantship in the Division of Computer Science, School of Electrical and Computer Engineering, NTUA, for the following courses: Data Structures (4th semester) and Parallel Processing Systems (9th semester).
- 2001-2005: Reviewer in many international scientific conferences, most notably IPDPS, ICS, HiPC, SAC etc.

- 1997: Laboratory assistantship in the Division of Computer Science, School of Electrical and Computer Engineering, NTUA, for the following courses: Computer Programming (1st semester), Programming Techniques (2nd semester) and Operating Systems (7th semester).
- 1996-2006: Teaching experience in preparing candidates for the Technical Schools of Germany (Abitur), England (International Baccalaureate, Bachelor thesis) and Greece (Panhellenic University Admission Examinations, NTUA Examinations).

PROFESSIONAL QUALIFICATIONS

- Embedded software development for mobile network backhauling equipment & ADSL2/ADSL2+ Access Multiplexers (DSLAMs).
- Design of Network Management System (NMS) on Linux architectures (PowerPC & MIPS).
- In-depth acquaintance with SNMP and Agent-X management protocols.
- Familiarization with IEEE/ITU/MEF telecommunication standards & RFCs.
- Programming in C, C++, Visual C++ (MFC), Visual Basic, UNIX shell.
- Parallel programming in MPI, OpenMP for PC SMP clusters.
- Experience on system/network administration in UNIX (Linux).
- Web development (HTML, Perl, PHP, Javascript, SQL).

LANGUAGES

- ENGLISH: Cambridge Certificate of Proficiency in English - 1995
- GERMAN: Deutsche Ergänzungsprüfung (Oberstufe-Abitur) - 1996
- ITALIAN: Certificato Superiore di conoscenza della lingua italiana - 1995

DISTINCTIONS

- 2002-2004: National Technical University of Athens Thomaidio prize for the publication in Scientific Journal or in the Proceedings of refereed Scientific conference.
- 2002-2005: EU "IRAKLITOS" scholarship for the PhD thesis (O.P. "Education", co-funded by the European Social Fund and National Resources).
- 2002-2004: Bodossaki Foundation Graduate Scholarship.
- 1996: State Scholarships Foundation scholarship for the admission rank in the National Technical University.
- 1996: Candidate for Deutscher Akademischer Austauschdienst scholarship (proposed by German School of Athens).

PUBLICATIONS

● JOURNALS

1. N. Drosinos and N. Koziris, "Efficient Hybrid Parallelization of Tiled Algorithms on SMP Clusters", International Journal of Computational Science and Engineering, pp. 112-126, 4(2), 2009.
2. G. Goumas, N. Drosinos and N. Koziris, "Communication-aware Supernode Shape", IEEE Transactions on Parallel and Distributed Systems, 20(4), pp. 498-511, April 2009.
3. N. Drosinos and N. Koziris, "The Effect of Process Topology and Load Balancing on Parallel Programming Models for SMP Clusters and Iterative Algorithms", Journal of Supercomputing, pp. 65-91, 35(1), January 2006.

4. G. Goumas, N. Drosinos, M. Athanasaki and N. Koziris, "Message-Passing Code Generation for Non-rectangular Tiling Transformations", Journal of Parallel Computing, pp. 711--732, 32(10), November 2006.
5. D. Souliou, A. Pagourtzis, N. Drosinos and P. Tsanakas, "Computing Frequent Itemsets in Parallel Using Partial Support Trees", Journal of Systems and Software, pp. 1735--1743, 79(12), December 2006.

● **CONFERENCES**

1. G. Goumas, N. Drosinos, V. Karakasis and N. Koziris, "Coarse-grain Parallel Execution for 2-dimensional PDE Problems", Proceedings of the 8th IEEE International Workshop on Parallel and Distributed Scientific and Engineering Computing (PDSEC-07), pp. 1--8, Long Beach, California, March 2007.
2. N. Drosinos, G. Goumas and N. Koziris, "Selecting the Tile Shape to Reduce the Total Communication Volume", Proceedings of the 20th International Parallel and Distributed Processing Symposium (IPDPS 2006), CD-ROM, Rhodes, Greece, April 2006.
3. N. Drosinos and N. Koziris, "Load Balancing Hybrid Programming Models for SMP Clusters and Fully Permutable Loops", Proceedings of the 7th International Workshop on High Performance Scientific and Engineering Computing (ICPP-HPSEC 2005), pp. 113--120, Oslo, Norway, June 2005.
4. N. Drosinos and N. Koziris, "Performance Comparison of Pure MPI vs Hybrid MPI-OpenMP Parallelization Models on SMP Clusters", Proceedings of the 18th International Parallel and Distributed Processing Symposium (IPDPS 2004), p. 15 (CD-ROM), Santa Fe, New Mexico, April 2004.
5. N. Drosinos and N. Koziris, "Advanced Hybrid MPI/OpenMP Parallelization Paradigms for Nested Loop Algorithms onto Clusters of SMPs", Proceedings of the 10th EuroPVM/MPI conference (EuroPVM/MPI 2003), pp. 203--213, Venice, Italy, September 2003.
6. N. Drosinos, G. Goumas, M. Athanasaki and N. Koziris, "Delivering High Performance to Parallel Applications Using Advanced Scheduling", Proceedings of the Parallel Computing 2003 (ParCo 2003), pp. 233--240, Dresden, Germany, September 2003.
7. D. Souliou, A. Pagourtzis and N. Drosinos, "Computing Frequent Itemsets in Parallel Using Partial Support Trees", Proceedings of the 12th EuroPVM/MPI conference (EuroPVM/MPI 2005), pp. 28--37, Sorrento, Italy, September 2005.
8. G. Goumas, N. Drosinos, M. Athanasaki and N.Koziris, "Automatic Parallel Code Generation for Tiled Nested Loops", Proceedings of the 19th Annual ACM Symposium on Applied Computing (SAC 2004), pp. 1412--1419, Nicosia, Cyprus, March 2004.
9. G. Goumas, N. Drosinos, M. Athanasaki, N.Koziris, "Data Parallel Code Generation for Arbitrarily Tiled Nested Loops", Proceedings of the 2002 International Conference on Parallel and Distributed Processing Techniques and Applications (PDPTA'02), pp. 360--369, Las Vegas, Nevada, USA, June 24-27, 2002.
10. G. Goumas, N. Drosinos, M. Athanasaki, N.Koziris, "Compiling Tiled Iteration Spaces for Clusters", Proceedings of the 4th IEEE International Conference on Cluster Computing (CLUSTER 2002), pp. 610--616, Chicago, Illinois, USA, September 24-26, 2002.

PRESENTATIONS

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- 2005: "Load Balancing Hybrid Programming Models for SMP Clusters and Fully Permutable Loops", 7th International Workshop on High Performance Scientific and Engineering Computing (ICPP-HPSEC 2005), Oslo, Norway
- 2004: "Performance Comparison of Pure MPI vs Hybrid MPI-OpenMP Parallelization Models on SMP Clusters", 18th International Parallel and Distributed Processing Symposium (IPDPS 2004), Santa Fe, New Mexico

2003: *“Advanced Hybrid MPI/OpenMP Parallelization Paradigms for Nested Loop Algorithms onto Clusters of SMPs”*, 10th EuroPVM/MPI conference (EuroPVM/MPI 2003), Venice, Italy

2003: *“Delivering High Performance to Parallel Applications Using Advanced Scheduling”*, Parallel Computing 2003 (ParCo 2003), Dresden, Germany

AFFILIATIONS

- Member of Technical Chamber of Greece.
- Volunteer for the International Amnesty.

MILITARY SERVICE

2006-2007: Hellenic Navy